®

Boruss® CPU Architecture

Software Developer’s Manual

Volume:

1

**NOTE:** This document contains Boruss® CPU Architecture Software Developer's Manual.

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CHAPTER 1

ABOUT THIS MANUAL

The Boruss CPU Architecture Software Developer’s Manual, Volume 1 describes the architecture and programming environment of Boruss® CPU architecture processor.

* 1. BORUSS® CPU PROCESSORS COVERED IN THIS MANUAL
* BorussCPU “Laibach”
  1. OVERVIEW OF VOLUME 1: BASIC ARCHITECTURE

A description of this manual’s content follows:

**Chapter 1 – About This Manual.** Gives an overview of the BorussCPU.

**Chapter 2 - Boruss® Architecture.** Introduces the Boruss CPU architecture and gives overview of the features.

**Chapter 3 – Basic Execution Environment.** Introduces the model of memory organization and describes the register set used by applications.

**Notices & Disclaimers**

Boruss CPU technology require use hardware